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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Shinya Hasegawa

Serial No.: Not yet assigned

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Title: SEMICONDUCTOR INTEGRATED CIRCUIT AND FUNCTIONAL
BLOCK OF THE SAME

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I hereby certify that this paper is being deposited with the United States Postal Service "EXPRESS MAIL Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Box PATENT APPLICATION, Commissioner for Patents; Washington, DC 20231.

Rosa A. Caviedes

Rosa A. Caviedes

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INFORMATION DISCLOSURE STATEMENT

Commissioner of Patents
Washington, DC 20231

Dear Sir:

Pursuant to 37 CFR §1.56(a), Applicant hereby cites the following documents (copies enclosed) listed on the attached copy of Form PTO-1449.

The Commissioner is also authorized to charge any deficiencies in fees and credit any overpayment of fees to Deposit Account No. 07-1896.

Respectfully submitted,

GRAY CARY WARE & FREIDENRICH LLP

By Edward B. Weller

Edward B. Weller
Reg. No. 37,468
Attorney for Applicant

GRAY CARY WARE & FREIDENRICH LLP
Patent Department
1755 Embarcadero Road
Palo Alto, CA 94303-3340
TEL: (650) 833-2436
FAX: (650) 320-7401